



PATENT  
P56585

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

SUNG-LYONG LEE *et al.*

Serial No.: 09/955,301                              Examiner: LEE, CHRISTOPHER E

Filed: 19 September 2001                              Art Unit: 2112

For: APPARATUS AND METHOD FOR CONNECTING BASE MODULE AND  
FUNCTION-EXTENDING MODULE IN AV SYSTEM

**INFORMATION DISCLOSURE STATEMENT**

**RECEIVED**

Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

MAY 20 2004

Technology Center 2100

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites, describes, and provides copies of the following art references:

**U.S. PATENT REFERENCE:**

- U.S. Patent No. 4,337,480 to Bourassin *et al.*, entitled DYNAMIC AUDIO-VIDEO INTERCONNECTION SYSTEM, issued on 29 June 1982.

**FOREIGN PATENT REFERENCE:**

- Japanese Patent Publication No. 02-246400 to Koshiishi, entitled BUS CONNECTOR AV RACK AND AV APPARATUS, published 2 October 1990.

**OTHER DOCUMENTS:**

- *Korean Office Action* from the Korean Intellectual Property Office issued in Applicant's corresponding Korean Patent Application No. 10-2001-0043503 (dated 27 April 2004).

**DISCUSSION**

As written in the Korean Office Action issued by the Korean Intellectual Property Office on the 27th April 2004 in applicant's corresponding Korean Patent application corresponding to applicant's above-captioned U.S. Patent Application, the Bourassin *et al.* US'480 patent was said to relate to a dynamic audio-video interconnection system for connecting together at least one television receiver and a plurality of peripheral units in home audio-visual installations. By means of a remote-control keyboard, the user transmits interconnection orders which are processed by a microprocessor in order to actuate electronic switches which effect the desired interconnection.

The Koshiishi JP'400 relates to AV apparatuses, etc., which can be inserted from a front face, a guide is provided inside side plates, and a multi-pin connector in which pins are connected in parallel (bus-connection) to be aligned longitudinally on the rear or inner faces of the side plates is provided.

The citation of the foregoing references is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relevant art.

Pursuant to 37 CFR § 1.97(d), the undersigned attorney hereby certifies that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign patent application not more than three(3) months prior to the filing of the statement.

No fee is incurred by this Statement.

Respectfully submitted,

  
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Date: 18 May 2004  
I.D.: REB/ny

INFORMATION DISCLOSURE STATEMENT PTO-1449 (PAGE 1 OF 1)		SERIAL NUMBER 09/955,301	DOCKET NO. P56585
		APPLICANT	SUNG-LYONG LEE et al.
		FILING DATE	19 September 2001
		GROUP	2112

## **U.S. PATENT DOCUMENTS**

EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	4,337,480	06/82	Bourassin <i>et al.</i>			
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## **FOREIGN PATENT DOCUMENTS**

## TRANSLATION

FOREIGN PATENT DOCUMENTS						INVENTION	
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	JP 02-246400	10/90	JAPAN			Abstract	

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)**

*Korean Office action* from the Korean Intellectual Property Office issued in Applicant's corresponding Korean Patent Application No. 10-2001-0043503 (dated 27 April 2004).

EXAMINER:

**DATE CONSIDERED:**

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.